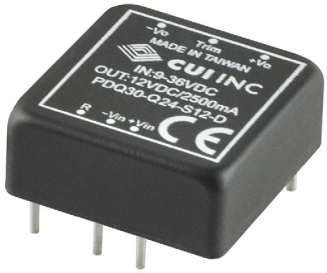
SERIES: PDQ30-D | **DESCRIPTION:** DC-DC CONVERTER

FEATURES

- up to 30 W isolated output
- industry standard 1" x 1" package
- single/dual regulated output
- over voltage, input under voltage lockout, and short circuit protections
- 1,500 Vdc isolation voltage
- remote on/off control
- output trim
- -40 to 105°C temperature range
- efficiency up to 90%
- certified to EN/IEC/UL 62368-1

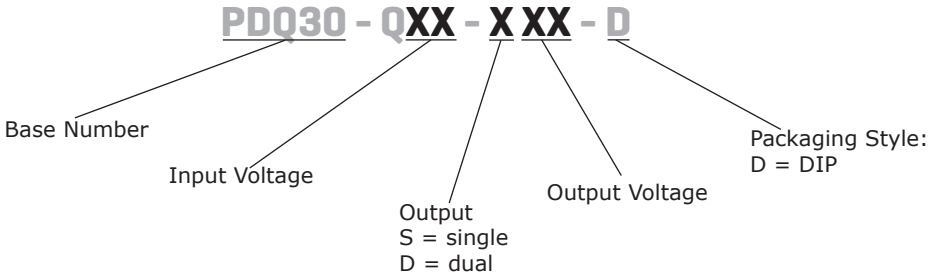


MODEL

MODEL	input voltage typ (Vdc)	input voltage range (Vdc)	output voltage (Vdc)	output current min (A)	output current max (A)	output power max (W)	ripple & noise[1] max (mVp-p)	efficiency typ (%)
PDQ30-Q24-S3-D	24	9~36	3.3	0	7.5	24.75	75	88
PDQ30-Q24-S5-D	24	9~36	5	0	6.0	30	75	90
PDQ30-Q24-S12-D	24	9~36	12	0	2.5	30	100	89
PDQ30-Q24-S15-D	24	9~36	15	0	2.0	30	100	89
PDQ30-Q24-D12-D	24	9~36	±12	0	±1.25	30	100	88
PDQ30-Q24-D15-D	24	9~36	±15	0	±1.0	30	100	88
PDQ30-Q48-S3-D	48	18~75	3.3	0	7.5	24.75	75	88
PDQ30-Q48-S5-D	48	18~75	5	0	6.0	30	75	90
PDQ30-Q48-S12-D	48	18~75	12	0	2.5	30	100	89
PDQ30-Q48-S15-D	48	18~75	15	0	2.0	30	100	89
PDQ30-Q48-D12-D	48	18~75	±12	0	±1.25	30	100	88
PDQ30-Q48-D15-D	48	18~75	±15	0	±1.0	30	100	89

Notes: 1. At full load, nominal input, 20 MHz bandwidth oscilloscope, with 10 µF tantalum and 1 µF ceramic capacitors on the output.
2. All specifications are measured at Ta=25°C, nominal input voltage, and rated output load unless otherwise specified.

PART NUMBER KEY



Please read our datasheet & drawing disclaimer here.

INPUT

parameter	conditions/description	min	typ	max	units
operating input voltage	24 Vdc input models 48 Vdc input models	9 18	24 48	36 75	Vdc Vdc
surge voltage	for maximum of 100 ms 24 Vdc input models 48 Vdc input models			 50 100	 Vdc Vdc
current	24 Vdc input models 48 Vdc input models			3.9 1.95	A A
under voltage shutdown	24 Vdc input models, power up 24 Vdc input models, power down 48 Vdc input models, power up 48 Vdc input models, power down		8.5 8.0 17 16		Vdc Vdc Vdc Vdc
remote on/off[1]	turn on (3.5~75 Vdc or open circuit) turn off (<1.2 Vdc)				
filter	pi filter				
input reverse polarity protection	no				
input fuse	6 A time delay fuse for 24 Vdc input models (recommended) 3 A time delay fuse for 48 Vdc input models (recommended)				

Notes: 1. CMOS or open collector TTL, reference to –Vin.

OUTPUT

parameter	conditions/description	min	typ	max	units
maximum capacitive load	3.3 Vdc output models 5 Vdc output models 12 Vdc output models 15 Vdc output models ±12 Vdc output models ±15 Vdc output models			7,500 6,000 2,500 2,000 1,250 1,000	µF µF µF µF µF µF
voltage accuracy				±1.5	%
line regulation	from high line to low line single output models dual output models			 ±0.2 ±0.5	 % %
load regulation	from full load to minimum load single output models dual output models			 ±0.2 ±1.0	 % %
voltage balance	dual output models			±1.5	%
cross regulation	load cross variation 10%/100% (dual output models)			±5	%
turn-on delay time, from input	from Vin, min to 10% Vo		10		ms
turn-on delay time, from on/off control	from Von/off to 10% Vo		10		ms
rise time	from 10% Vo to 90% Vo		10		ms
adjustability[2]	see application notes		±10		%
switching frequency	3.3, 5 Vdc output models all other models		270 330		kHz kHz
dynamic load response	75%-100% step load change error band (Vout) recovery time		 5 250		 % µs
temperature coefficient			±0.03		%/°C

Note: 2. For single output models only.

PROTECTIONS

parameter	conditions/description	min	typ	max	units
over voltage protection	zener or TVS clamp 3.3 Vdc output models 5 Vdc output models 12 Vdc output models (single and dual) 15 Vdc output models (single and dual)		 3.9 6.2 15 18		 Vdc Vdc Vdc Vdc
over current protection	hiccup mode	110	140	170	%
short circuit protection	continuous, automatic recovery				
over temperature protection	output shutdown, automatic recovery		110		°C

SAFETY AND COMPLIANCE

parameter	conditions/description	min	typ	max	units
isolation voltage	input to output for 1 minute	1,500			Vdc
isolation resistance	input to output	1,000			MΩ
isolation capacitance	input to output		1,500		pF
safety approvals[3]	certified to 62368-1: EN, IEC, UL				
conducted emissions	EN 55032 Class A (external circuit required, see Figure 3)				
RoHS	yes				

Note: 3. Output is only considered as ES1 if the input source meets reinforced insulation requirements from primary AC source or any hazardous voltage source. Unit maintains only functional insulation from input to output

ENVIRONMENTAL

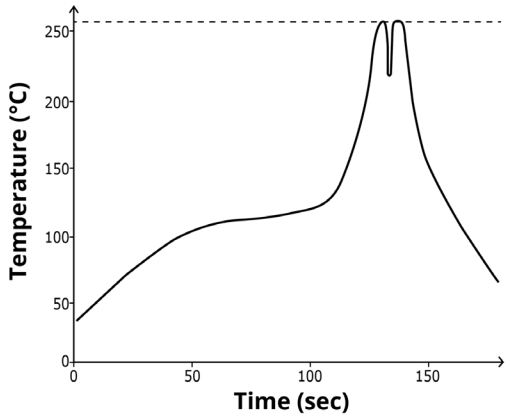
parameter	conditions/description	min	typ	max	units
operating temperature	see derating curves	-40		105	°C
storage temperature		-55		125	°C
operating humidity	non-condensing			95	%

SOLDERABILITY

parameter	conditions/description	min	typ	max	units
wave soldering	see wave soldering profile			260	°C

Notes: 1. Soldering materials: Sn/Cu/Ni
2. Ramp up rate during preheat: 1.4°C/s (from 50°C to 100°C)
3. Soaking temperature: 0.5°C/s (from 100°C to 130°C), 60±20 seconds
4. Peak temperature: 260°C, above 250°C for 3~6 seconds
5. Ramp down rate during cooling: -10°C/s (from 260°C to 150°C)

WAVE SOLDERING PROFILE



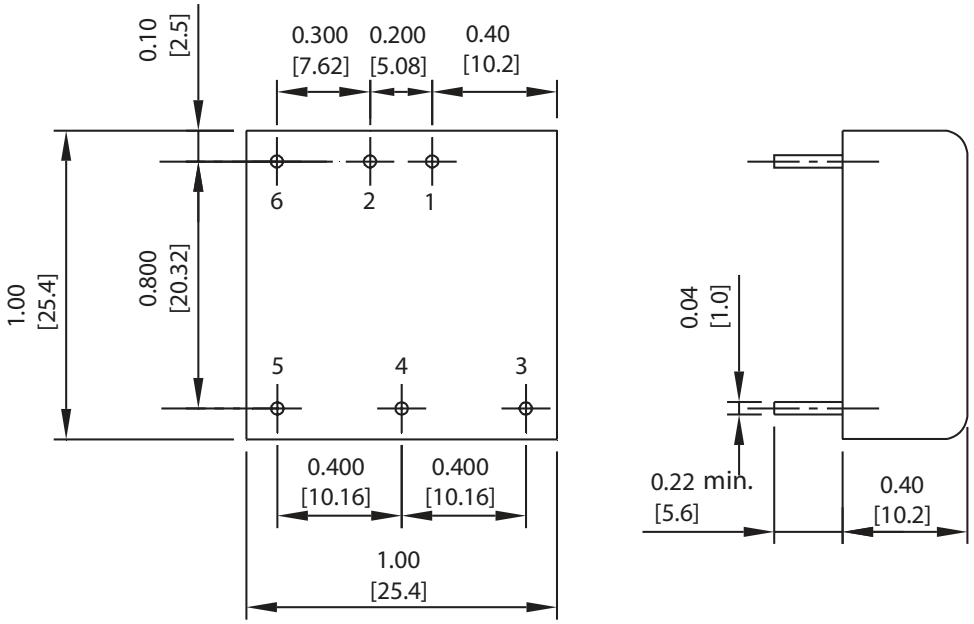
MECHANICAL

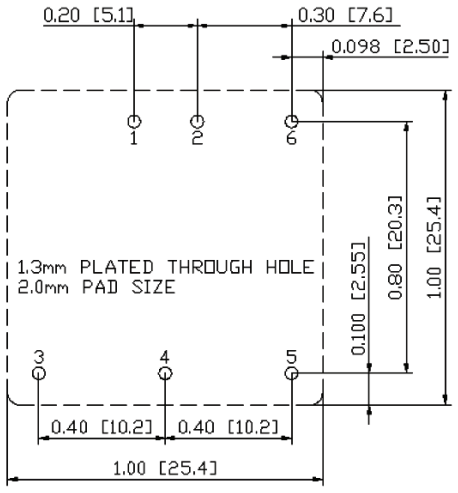
parameter	conditions/description	min	typ	max	units
dimensions	1.00 x 1.00 x 0.4 [25.4 x 25.4 x 10.2 mm]				inches
case material	black coated copper with non-conductive base				
weight			18		g

MECHANICAL DRAWING

units: inches [mm]
tolerance: X.XX ±0.02 [±0.5]
X.XXX ±0.010 [±0.25]
pin diameter tolerance: ±0.004[±0.1]

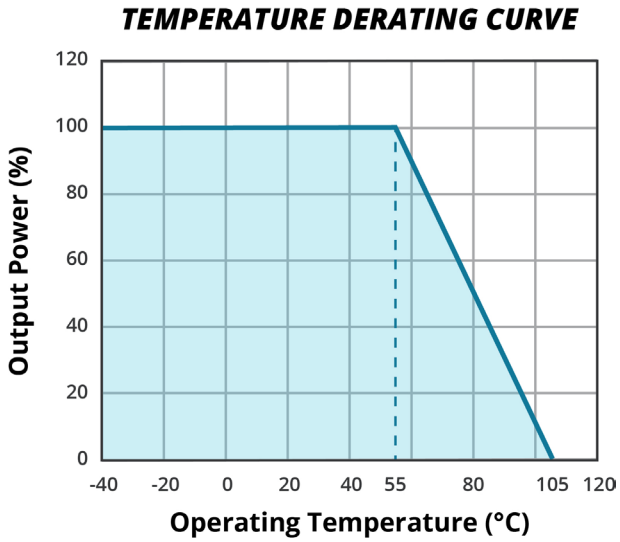
PIN CONNECTIONS		
PIN	Function	
	Single	Dual
1	+Vin	+Vin
2	-Vin	-Vin
3	+Vout	+Vout
4	Trim	Common
5	-Vout	-Vout
6	Remote	Remote



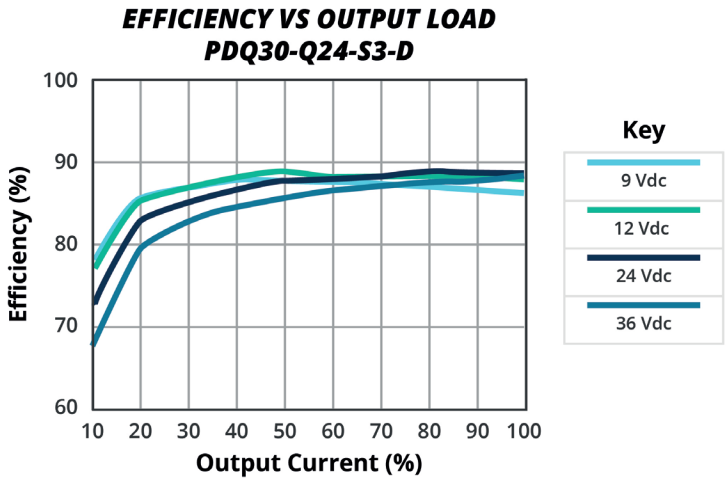


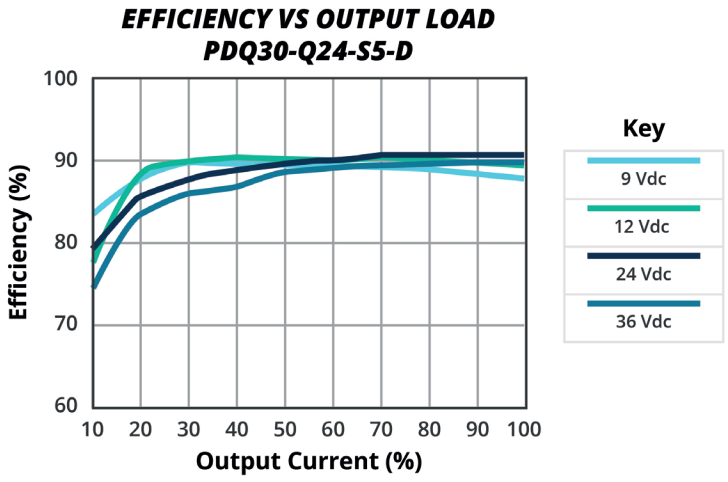
Recommended PCB Layout
Top View

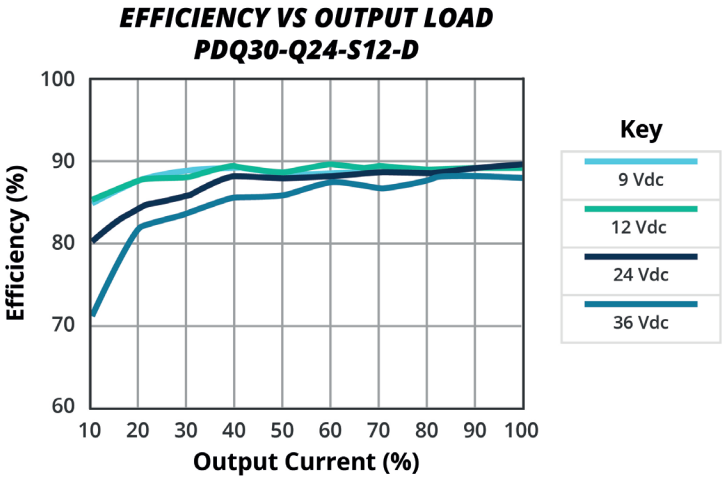
DERATING CURVE

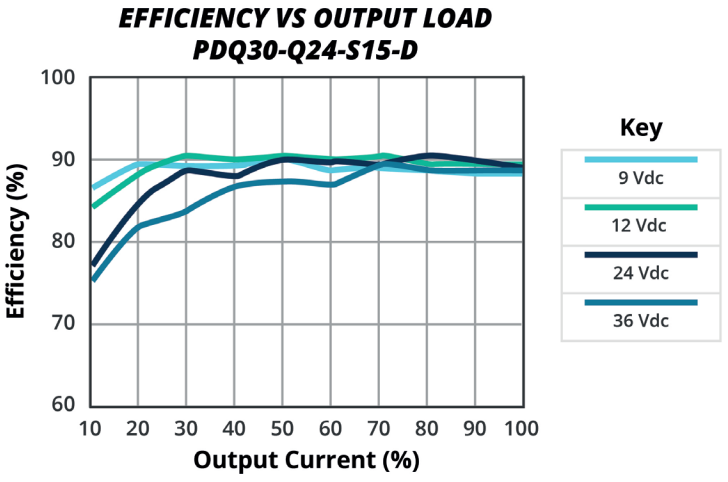


EFFICIENCY CURVES

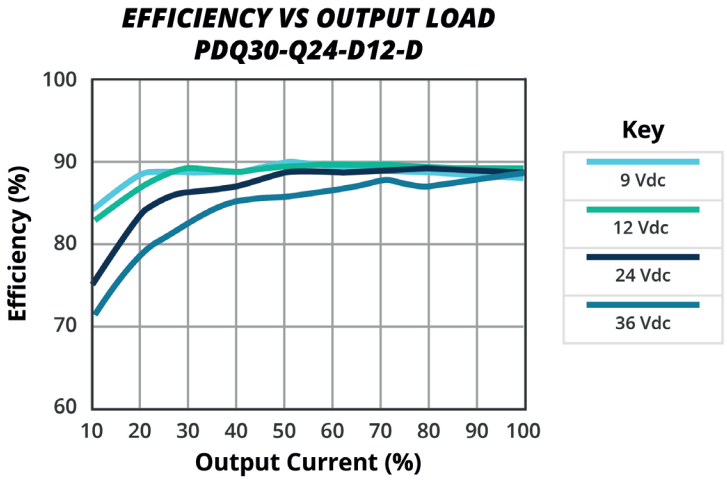


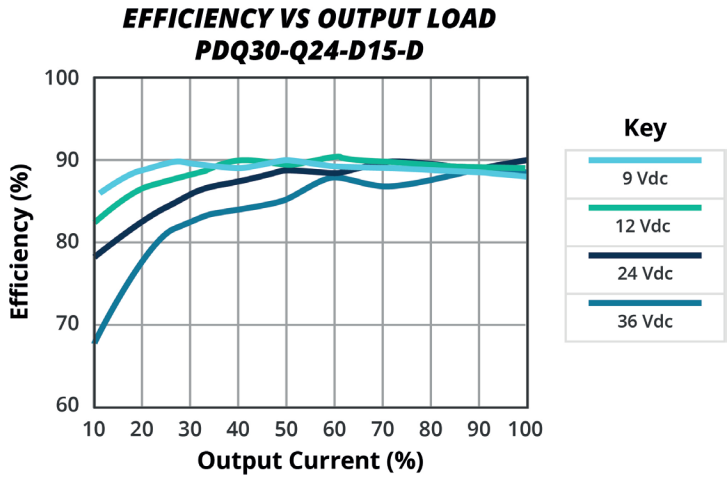


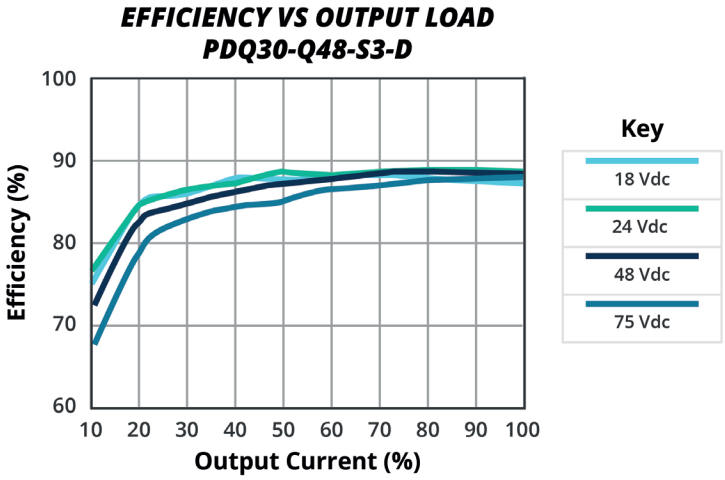


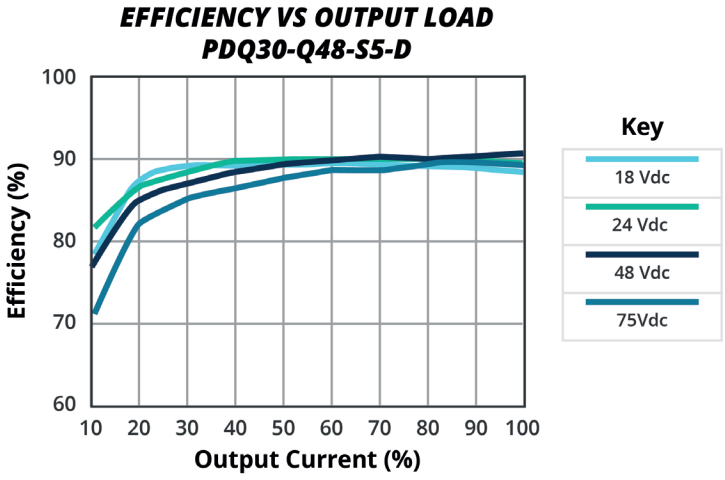


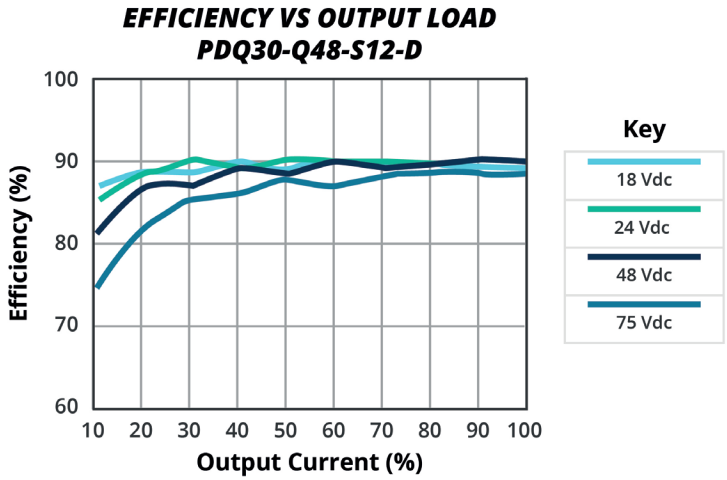
EFFICIENCY CURVES (CONTINUED)

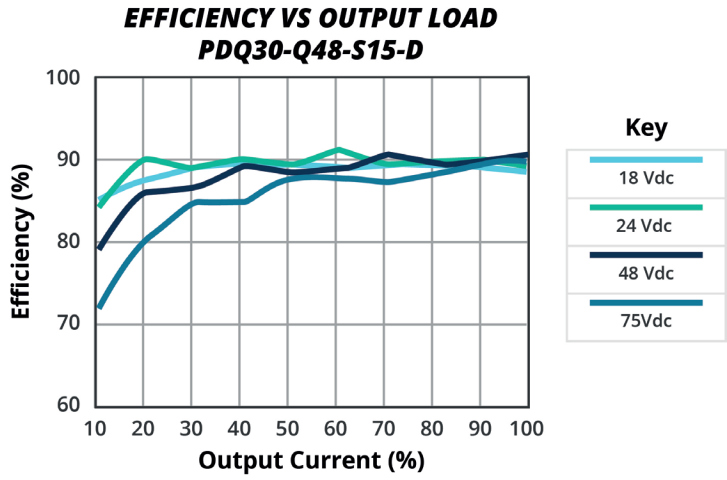




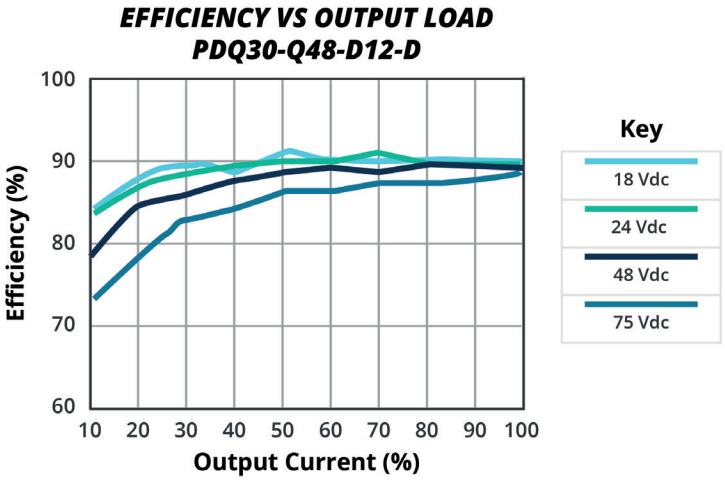


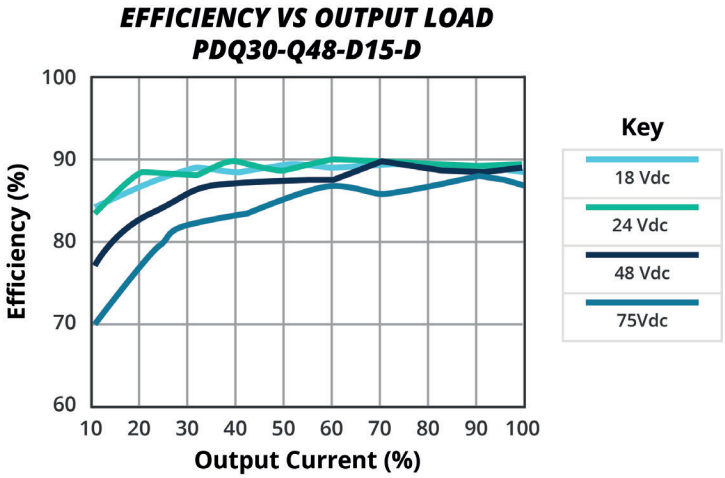






EFFICIENCY CURVES (CONTINUED)





TEST CONFIGURATIONS

Input Ripple Current & Output Noise

Figure 1 Measuring Input Ripple Current

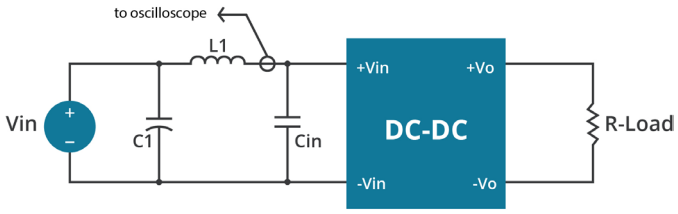


Figure 2 Measuring Output Ripple And Noise

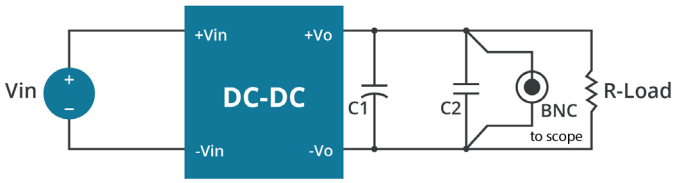


Table 1

L1	12 µH
C1	220 µF ESR < 0.1 Ω at 100 kHz
Cin	33 µF ESR < 0.7 Ω at 100 kHz

Table 2

C1	10 µF tantalum capacitor
C2	1 µF ceramic capacitor

EMC RECOMMENDED CIRCUIT

Test Condition
Input Voltage: Nominal
Output Load: Full Load

Figure 3 Conducted Emissions Test Circuit

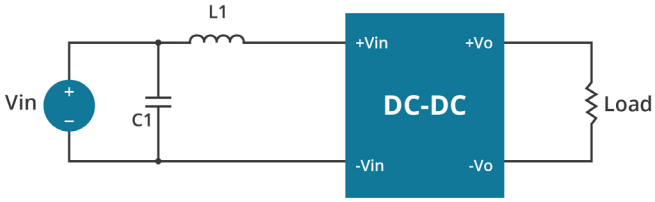


Table 3

EN55022 Class A Recommended External Circuit Components		
Input Voltage (Vdc)	C1	L1
24	100 µF / 50 V	0.47 µH
48	4.7 µF / 100 V	2.2 µH

APPLICATION NOTES

Output Voltage Trimming

The output voltage can be adjusted (single outputs only) by using the trim pin and the use of either an external trim pot or the use of a single fixed resistor (see Figures below). If the trim function is not needed, leave the trim pin open.

Figure 4 Trim Adjustments Using A Trimpot

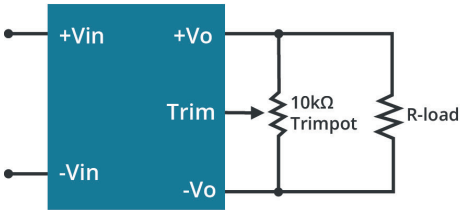


Figure 5 Trim Adjustments To Increase Output Voltage Using A Fixed Resistor

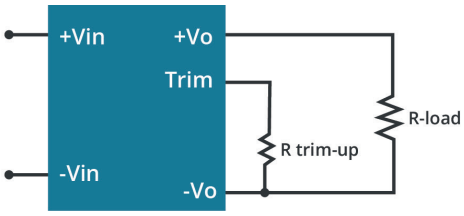
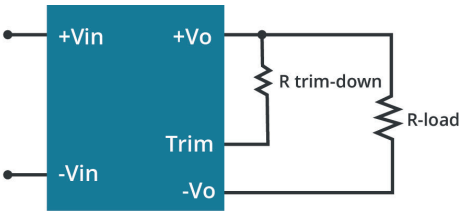


Figure 6 Trim Adjustments To Decrease Output Voltage Using A Fixed Resistor



$$R_{TRIM} = \left[\frac{V_{REF} \times R_{TOP} \times (R_{BOTTOM} + R_O)}{R_{BOTTOM} \times (V_{OUT} - V_{OUT,\,NOM})} \right] - R_T \; (K\,\Omega)$$

Formula for Trim up

$$R_{TRIM} = R_{TOP} \times \left[\frac{V_{REF} \times R_{TOP}}{R_{BOTTOM} \times (V_{OUT,\,NOM} - V_{OUT})} - 1 \right] - R_T \quad (K\,\Omega)$$

Formula for Trim down

Table 4

V_{NOM}	R_{TOP}	R_{BOTTOM}	R_O	R_T	V_{REF}
(Vdc)	(kΩ)	(kΩ)	(kΩ)	(kΩ)	(V)
3.3	2.74	1.8	0.27	9.1	1.24
5	2.32	2.32	0	8.2	2.5
12	6.8	2.4	2.32	22	2.5
15	8.06	2.4	3.9	22	2.5

Note: Value for R_{TOP}, R_{BOTTOM}, R_O, R_T and V_{REF} refer to Table 4 (fixed internal values).
R_{TRIM}: Trim resistance
a: User-defined parameter, no actual meanings
V_{NOM}: Nominal output voltage
V_{OUT}: Target output voltage

REVISION HISTORY

rev.	description	date
1.0	initial release	07/12/2016
1.01	added 5 Vdc output efficiency curves	09/04/2018
1.02	safeties updated	05/25/2021
1.03	derating curve and circuit figures updated	03/14/2022
1.04	output voltage trimming updated	06/15/2023
1.05	features, safety approvals & input voltage of PDQ30-Q48-D12-D updated	09/11/2024
1.06	safeties updated	01/16/2025
1.07	safeties updated	04/29/2025
1.08	conducted emissions updated	07/25/2025

The revision history provided is for informational purposes only and is believed to be accurate.

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